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INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)



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| ATTY DOCKET NO. 03226/037001 | | SERIAL NO. 09/778,424 |
| Joseph C. H. PARK | | |
| FILING February 7, 2001 | GROUP 2185 | |

U.S. PATENT DOCUMENTS

| *EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
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FOREIGN PATENT DOCUMENTS

| DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION | |
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

| | | |
|----|----|---|
| AT | B1 | Bernstein, David & Rodek, Michael; "Global Instruction Scheduling for Superscalar Machines" ACM SIGPLAN '91 Conference on Programming Language Design & Implementation, Toronto, Ontario, Canada, June 26-28, 1991, Pages 241-255 |
| AT | B2 | "Intel 1A-64 Architecture Software Developer's Manual, Volume 1: 1A-64 Application Architecture" January 2000; Pages iii-12-14 |

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|--------------|------------------------------|
| EXAMINER | DATE CONSIDERED 3-20-2004 |
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PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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|----|--|----|---|
| AA | | C1 | Park, Joseph, C. H.; Schlanser, Mike; "On Predicted Execution"; Hewlett Packard, Software and Systems Laboratory, HPL-91-58; May 1991; Pages 1-25 |
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EXAMINER

DATE CONSIDERED

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